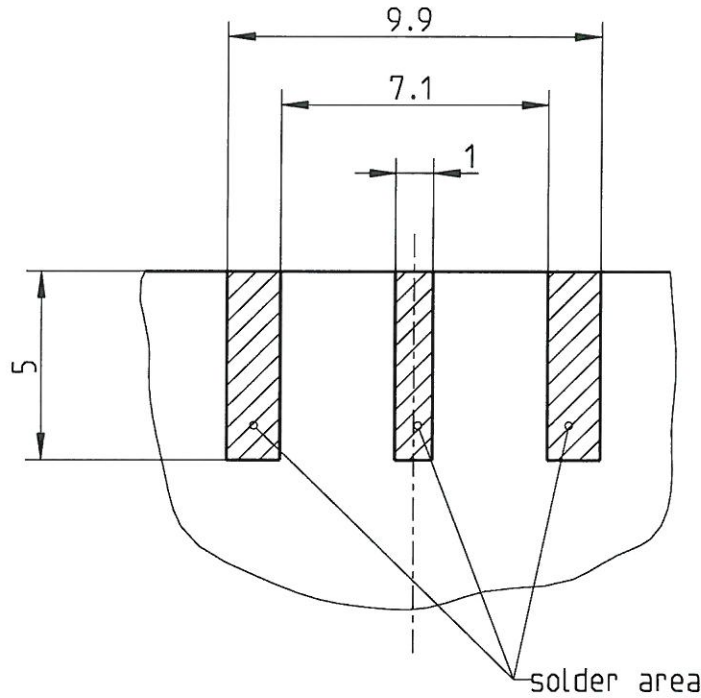


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Leiterplatten-Layout
PCB layout
B 206



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A wide variety of transmissionline topologies and pcb-parameters like permittivity, substrate thickness, and board-stackup are applied by customers. These parameters have a strong impact on the high frequency performance of the mounted connector.

Please note, that the given layout is not optimised to fit all of the possible board configurations regarding RF-performance, it represents a recommendation for optimum solderability of the connector.

In order to guarantee optimum high frequency properties of the connector, an RF-analysis of the connector to board transition is recommended.

Formzahl: TCC-FB-05-PC-A-Einzelteil
Datei: A1_EINZELTEILJOB.BRM
Version: 1.0

-METRIC-



ISO-Projektion
Methode E

Rosenberger Hochfrequenztechnik 84526 Tiltmoning Pro/ENGINEER		general tolerance ISO 2768 RN 006-01 m-H dimensions <0.5 and symmetry		scale: 5:1	weight(g): surface(mm²):
				material:	
		date name drawn 15.11.2005 A_Nobis check. 30.11.05 AL7 appr. 21/12/05 B...		title: Leiterplatten-Layout PCB layout	
		dimensioning incl. finish		part-no...: MB_206	
a00	05-0615	A_Nobis	15.11.2005	distribu- tion to:	sheet: 1 of: 1
rev.	change-no	name	date	FE AZ QSM RMT . X	remarks: .